

UTILITY PATENT APPLICATION TRANSMITTAL <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No. 240438US2	
	First Inventor or Application Identifier Hirokazu SAYAMA, et al.	
	Title SEMICONDUCTOR DEVICE INCLUDING GATE ELECTRODE FOR APPLYING TENSILE STRESS TO SILICON SUBSTRATE, AND METHOD OF MANUFACTURING THE SAME	
Assignee Name: Renesas Technology Corp.		19249 U.S. PTO 10/620379 07/17/03
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APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	ADDRESS TO: Commissioner for Patents Mail Stop Patent Application Alexandria, Virginia 22313
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS
2. <input checked="" type="checkbox"/> Specification Total Sheets <input type="text" value="29"/>	7. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))
3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets <input type="text" value="14"/>	8. <input checked="" type="checkbox"/> Application Data Sheet. See 37 CFR 1.76
4. <input checked="" type="checkbox"/> Oath or Declaration Total Pages <input type="text" value="4"/>	9. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement (when there is an assignee) <input type="checkbox"/> Power of Attorney
a. <input checked="" type="checkbox"/> Newly executed (original)	10. <input type="checkbox"/> English Translation Document (if applicable)
b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)	11. <input checked="" type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input checked="" type="checkbox"/> Copies of IDS Citations (4)
i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).	12. <input type="checkbox"/> Preliminary Amendment
5. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)	13. <input checked="" type="checkbox"/> White Advance Serial No. Postcard
6. <input type="checkbox"/> Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)	14. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (1) (if foreign priority is claimed)
a. <input type="checkbox"/> Computer Readable Form (CRF)	15. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27
b. Specification or Sequence Listing on :	16. <input checked="" type="checkbox"/> Other: Request for Priority
i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or	
ii. <input type="checkbox"/> Paper	
c. <input type="checkbox"/> Statements verifying identity of above copies	

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application no.: _____

Prior application information: Examiner: _____ Group Art Unit: _____

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.


18. Amend the specification by inserting before the first line the sentence:

☐ This application is a ☐ Continuation ☐ Division ☐ Continuation-in-part (CIP)
of application Serial No. _____ Filed on _____

☐ This application claims priority of provisional application Serial No. _____ Filed _____

19. CORRESPONDENCE ADDRESS


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18334 U.S. P.
07/17/03

Pocket No. 240438US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hirokazu SAYAMA, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR DEVICE INCLUDING GATE ELECTRODE FOR APPLYING TENSILE STRESS
TO SILICON SUBSTRATE, AND METHOD OF MANUFACTURING THE SAME

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	18 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x \$84 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$280 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEE				\$750.00
TOTAL OF ABOVE CALCULATIONS				\$750.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
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TOTAL				\$790.00

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☒ A check in the amount of \$790.00 to cover the filing fee is enclosed.

☒ The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
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